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May 25, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Hoffman, G.
Serial Number	: 10/081,630
For	: APPARATUS AND METHOD FOR TESTING BATTERY CONDITION
Filed	: 02/21/02
Examiner	: LUK, LAWRENCE.
Art Unit	: 2838

LETTER IN RESPONSE TO EXAMINER'S QUERY

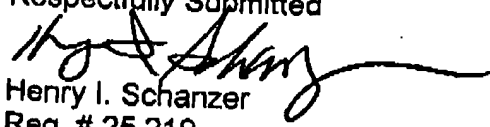
The Examiner, Mr. Luk, just called my office with an indication that he had not received a response to the office action of 10/03/03 and that this note and a copy of the amendment should be faxed to the number indicated above..

Please be advised that the attached amendment was sent to the USPTO on 12/29/03.

As further evidence, attached is a copy of the postcard indicating receipt of our response by the by the USPTO on De. 31, 2003. Also enclosed are copies of the front and back of a check for \$9.00 which had been sent with the amendment and which was cashed by the USPTO.

I trust that this will clear up this matter. If there are nay questions please contact me as indicated above.

Respectfully Submitted


Henry I. Schanzer
Reg. # 25,219

PLEASE CONFIRM RECEIPT OF THIS FAX.

10/081, 630
APPARATUS & METHOD
FOR TESTING BATTERY
CONDITION - G-3
GARY HOFFMAN
AMENDMENT
\$4.00 for Additional
Claim



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REMITTANCE ADVICE	
10/081,630	G-3
Amendment	

55-216/212

2427

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12/29/03	Commissioner for Patents & Trademarks	10/081,630 Amendment G-3	2427	

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Hoffman g-3-amendment

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Name of Rep. HENRY SCHANZER

Signature [Signature]

Date 12/29/03

s/n: 10/081,630

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AMENDMENT

In response to the office action dated 10/03/03, please amend the above-identified application as follows:

circuitry and wherein the microprocessor circuitry is programmed to determine the value of the battery resistance.

5. (original) In the system as claimed in claim 1, wherein the battery is a first battery for supplying power to the load, with a load current from the first battery flowing through the load within a first loop; and wherein the current source generating a first current includes a second power source for supplying said first current and wherein said first current flows through a second loop which includes the first battery and said second power source.
6. (currently amended) In the system as claimed in claim 5, wherein the first battery and the second power source are connected in series, with the first battery and the second power source poled so their voltages are adding
7. (currently amended) In the system as claimed in claim 5, wherein the first battery and the second power source are connected in series, with the first battery and the second power source poled so their voltages are opposing.
8. (currently amended) In the system as claimed in claim 5, wherein the first battery, the second power source, the current source, the selectively enabled switch and the resistor are connected in series defining said second conductive loop for selectively passing said first current.

IN THE CLAIMS:

1. (currently amended) In a system in which a load is connected across first and second power terminals of a battery, a circuit for sensing the condition of the battery comprising:
 - a current source generating a first current;
 - a resistor;
 - a selectively enabled ~~current~~ switch,
 - means connecting the current source in series with the resistor, the selectively enabled ~~current~~ switch, and the battery for selectively passing the first current through the resistor and the battery;
 - means for sensing the voltage across the resistor; and
 - means for sensing the voltage across the battery for the condition when the selectively enabled switch is open and for the condition when the selectively enabled switch is closed.
2. (original) In the system as claimed in claim 1, wherein the voltage is sensed across the resistor to determine the amplitude of the first current.
3. (original) In the system as claimed in claim 2, wherein the values of the voltages sensed across the battery when the selectively enabled switch is closed and then opened and the value of the voltage sensed across the resistor are for determining the value of the resistance of the battery.
4. (original) In the system as claimed in claim 1, wherein the voltage sensed across the resistor is coupled to microprocessor circuitry and wherein the voltage sensed across the battery is also coupled to the microprocessor